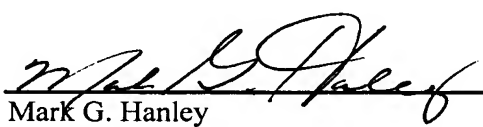




IFW

PATENT
Docket No. INTEL/17879

**IN THE UNITED STATES PATENT
AND TRADEMARK OFFICE**

Applicant(s):)	I hereby certify that this paper and the
)	documents referred to as enclosed
Tu et al.)	therewith are being deposited with the
)	United States Postal Service as first
U.S. Serial No.: 10/730,467)	class mail, postage prepaid, in an
)	envelope addressed to Commissioner
For: "Methods and Apparatus to)	for Patents, P.O. Box 1450,
Dispatch Interrupts in Multi-)	Alexandria, Virginia 22313-1450 on
Processor Systems")	this date:
)	
Filed: December 8, 2003)	February 9, 2005
)	
Assignee: Intel Corporation)	
)	
Group Art Unit: 2181)	
)	Mark G. Hanley
Examiner: Not Yet Assigned)	Attorney for Applicant(s)
)	Registration No. 44,736

STATUS LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

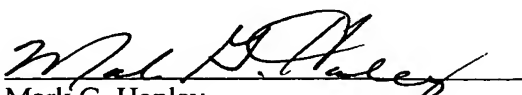
Kindly advise when an Office action can be expected in the above-referenced matter. Please note that it has been more than seven months since the last communication.

Respectfully submitted,

HANLEY, FLIGHT & ZIMMERMAN, LLC
USPTO Customer Number 34431
20 North Wacker Drive
Suite 4220
Chicago, Illinois 60606
(312) 580-1020

February 9, 2005

By:


Mark G. Hanley
Registration No.: 44,736